

Analog Devices Training

EMBEDDED PROCESSOR/DSP WORKSHOPS

Minimize time to market
Maximize design effectiveness
Better utilization of project resources
Reduce project costs by reducing total development time

Design changes cost money and time. By thoroughly understanding the processor architecture and its development environment a more effective systems solution can be selected earlier, minimizing design changes later.

Our full 3.5 day workshop speeds you through a large part of the learning curve associated with the adoption of new technology.

Training includes in-depth coverage of a specific Embedded Processor/DSP architecture and peripheral set, VDSP++ development tools, and the application development process.

Our workshops ensure that the processor and tools capabilities are understood well enough to enable making informed systems design decisions

In addition to the regularly scheduled workshops in North America and Europe, training can also be arranged to be onsite at your facilities anywhere in the world.

Onsite workshops can be tailored to focus on architectural aspects relevant to your application. A recommended systems approach can be suggested and overall program risk can be minimized by discussing the pro's and con's of a particular design implementation as part of a custom training session.

*Providing ADI Processor
Focused Training,
Products, and
Systems Development
Support*



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Workshop Overview

The standard workshop topics include:

- ◆ **Processor Core Architecture**
 - ◆ Computational capability, data fetching, sequencing (i.e. branching, looping, interrupt handling)
- ◆ **Peripheral set for a specific processor**
 - ◆ External bus, serial/parallel I/O, configuration
- ◆ **DMA (Direct Memory Access)**
- ◆ **VisualDSP++ Development Environment**
 - ◆ Projects, compiler, assembler, development process
- ◆ **Linker Operation**
 - ◆ Getting code/data mapped into target memory
- ◆ **VDK (VisualDSP Kernel) Introduction**
- ◆ **System Services/Devices Drivers Introduction**
- ◆ **The Boot process**
 - ◆ Including creating/flashing boot image files
- ◆ **Hardware Development Tools**
 - ◆ ICE (In Circuit Emulator), EZKITS
- ◆ **Optimization techniques**
 - ◆ Compiler, system optimization, 'C' callable assembly

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Application specific training modules

Benefits of onsite workshops include application specific customization of the standard modules.

Examples include:

- ◆ greater focus on VDK (detailed code examples)
- ◆ setting up specific data flow frameworks with DMA
- ◆ working with specific peripherals

Contact us regarding your application specific training requirements and we will work with you to create an effective training program.

For more Information:

For information on workshop schedules, course outlines, locations, and registration, please visit www.kaztek.com. For onsite training requests, or any other questions, please contact Kaztek Systems at 978-929-2900 or by email at info@kaztek.com.